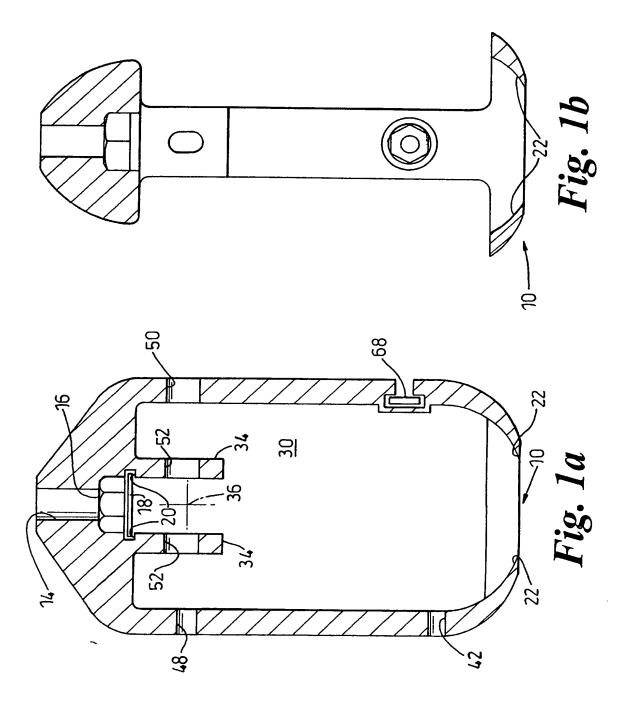
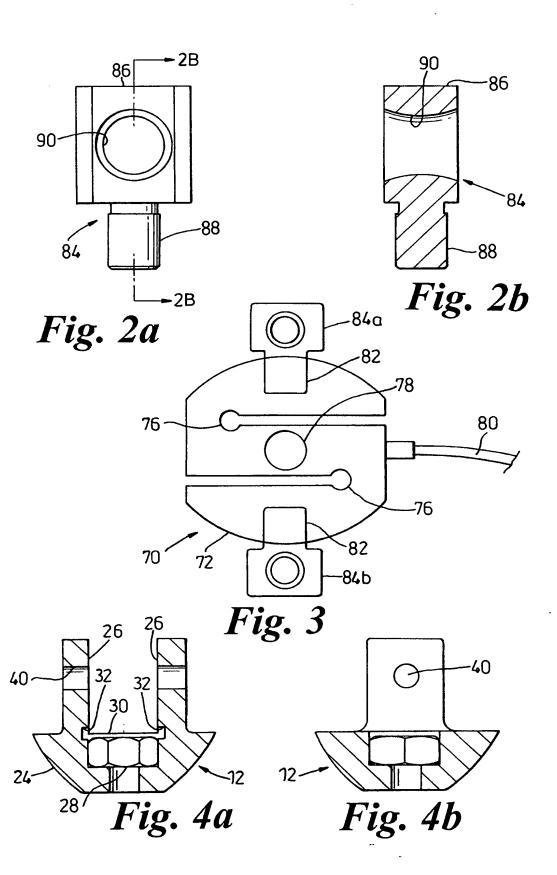


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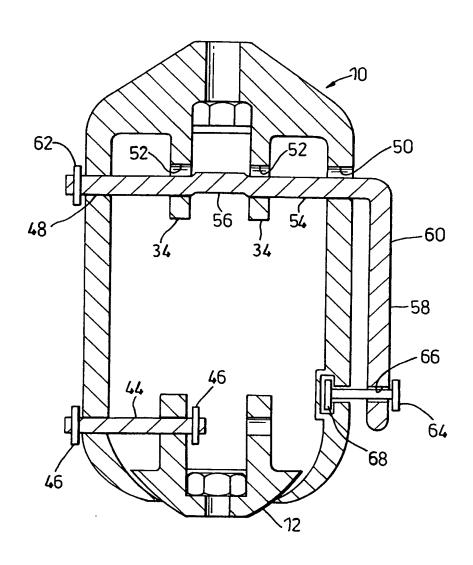
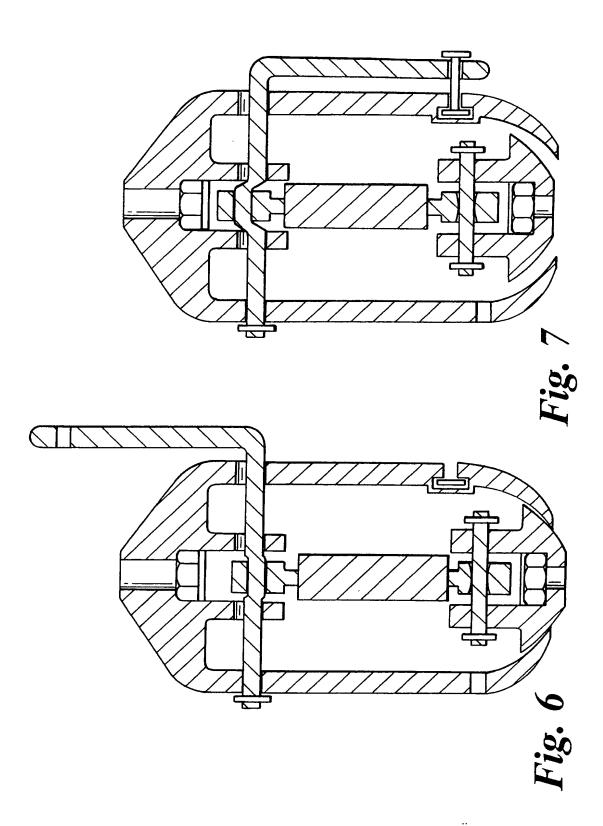


Fig. 5



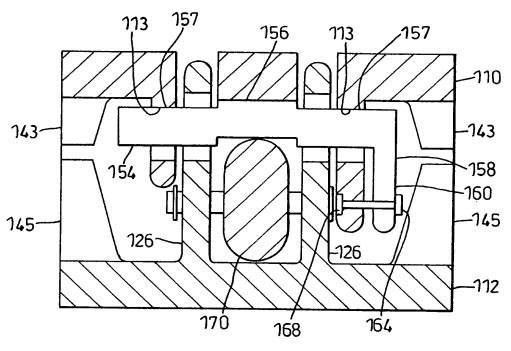


Fig. 8

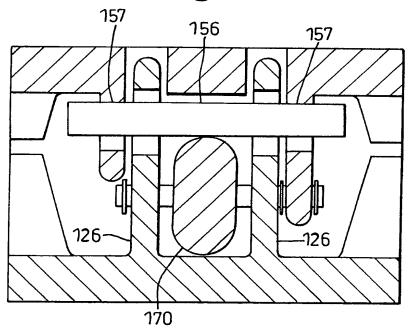


Fig. 9

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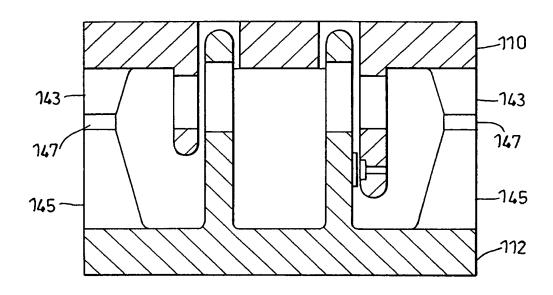


Fig. 10a

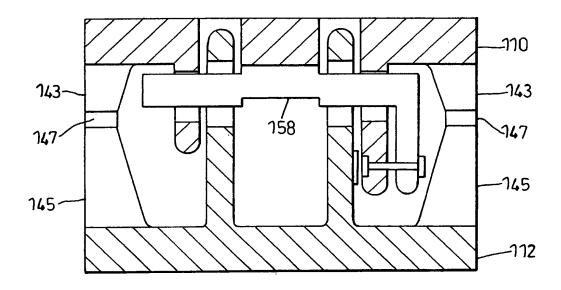


Fig. 10b

Intel. LOAD ASSEMBLY
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